Docket No.: 4670-0114PUS1

(Patent)

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application of:

Hidekazu MORI et al.

Application No.:

10/562,554

Confirmation No.: 8229

Filed:

December 30, 2006

Art Unit:

2823

For:

METHOD FOR PRODUCING ELECTRODE

Examiner:

K. Parendo

FOR ELECTRIC DOUBLE LAYER

**CAPACITOR** 

## LETTER ENCLOSING JAPANESE OFFICE ACTION AND TRANSLATION

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

This letter is further to the Information Disclosure Statement filed on August 6, 2010. Applicants note that the Examiner lined through the listing of the Japanese Office Action on the PTO SB08 attached to the Notice of Allowance. The Examiner indicated that Japanese Office Action was lined through for not being submitted in the English language. Applicants hereby submit an English translation of the Japanese Office Action, along with an English Machine Translation of the JP 11-162794. Applicants also reference US 2003/0158310A1 which corresponds to WO 01/078171 which was cited in the IDS filed August 6, 2010. A copy of a new SB08 is attached for the Examiner's consideration. The Examiner is respectfully requested to initial and sign the attached new SB08 form and make the signed SB08 form of record.

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Should there be any outstanding matters that need to be resolved in the present application, the Examiner is respectfully requested to contact Garth M. Dahlen, Ph.D., Esq., Reg. No. 43,575, at the telephone number of the undersigned below to conduct an interview in an effort to expedite prosecution in connection with the present application.

If necessary, the Director is hereby authorized in this, concurrent, and future replies to charge any fees required during the pendency of the above-identified application or credit any overpayment to Deposit Account No. 02-2448.

Dated: January 3, 2011

Respectfully submitted,

Marc S. Weiner

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Attachment